

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Haruo Hyodo et al.                      Art Unit : Unknown  
Serial No. :    Examiner : Unknown  
Filed : September 26, 2001  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEROF

Commissioner for Patents  
Washington, D.C. 20231



2/IDS  
P. Walker  
11-14-01

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, a copy of which is enclosed. The relevance of the reference is discussed on page 1 of the present specification.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Chris T. Mizumoto  
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Date: September 26, 2001

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